

IN THE SPECIFICATION

Amendments to the Specification:

Please replace the paragraph beginning page 7, line 5, with the following edited paragraph:

--In still another embodiment, a system using a memory generator for making a memory device that has reduced bitline voltage offsets is disclosed. The system includes an automated circuit generator means for providing a memory core that has a depth that defines a plurality of words, and a word width that is defined by multiple pairs of a global bitline and a global complementary bitline. The system also includes means an automated circuit generator for designing a core cell having a bitline and a complementary bitline, and means an automated circuit generator for designing a flipped core cell that has a flipped bitline and a flipped complementary bitline. The system further includes means an automated circuit generator for arranging a core cell followed by a flipped core cell along each of the multiple pairs of the global bitlines and the global complementary bitlines.--